Future Technology Devices International Ltd.

TN_137 FT220X Errata Technical Note

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Version 1.2

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The intention of this errata technical note is to give a detailed description of known functional or electrical issues with the FTDI FT220X devices.
The current revision of the FT220X is **revision C, released June 2012.**

Future Technology Devices International Limited (FTDI)

Unit1, 2 Seaward Place, Centurion Business Park, Glasgow G41 1HH United Kingdom
Tel.: +44 (0) 141 429 2777 Fax: + 44 (0) 141 429 2758
E-Mail (Support): **support1@ftdichip.com** Web: http://www.ftdichip.com

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1 FT220X Revision

FT220X part numbers are listed in **Table 1.** The letter at the end of date code identifies the device revision.

The current revision of the FT220X is **revision C, released June 2012.** At the time of releasing this Technical Note there is one known issue with this silicon revision.

Part Number	Package
FT220XQ	16 pin QFN
FT220XS	16 pin SSOP

Table 1 FT220X Part Numbers

This errata technical note covers the revisions of FT220X listed in **Table 2**.

Revision	Notes
A	First device revision. Never sold publicly.
B	Second device revision. Launched 28 February 2012
C	Third device version. Released 11th June 2012

Table 2 FT220X Revisions

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2 Errata History Table – Functional Problems

Functional Problem	Short description	Errata occurs in device revision
USB Data Transfer	Transfer of data over USB stops unexpectedly	A, B and C

Table 3 Functional Errata

2.1 Errata History Table – Electrical and Timing Specification Deviations.

Deviations	Short description	Errata occurs in device revision
Fault with internal 3V3 regulator.	Device VCC is designed to operate between 3V3 and 5V however with this errata the supply should not be set below 4.3V for correct operation.	B

Table 4 Electrical and Timing Errata

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3 Functional Problems of FT220X

3.1 Revision A

3.1.1 USB Data Transfer

Introduction:

An issue has been identified where the transfer of data over USB stops unexpectedly.

Problem:

The device is put into suspend mode during a transfer of certain data patterns most notable with binary zeros. This can halt the data transfer in certain circumstances and will require the device to be re-enumerated to recover.

Workaround:

This issue can be avoided by utilising the keep awake function of the chip. This will disable the USB suspend function of the chip and is therefore an intermediate workaround until revision D silicon is released with a permanent fix.

NB. With the workaround the chip will never enter lower powered suspend. However the keep awake current will be approximately 3mA.

To enable the keep awake function in the EEPROM, one of the CBUS pins needs to be configured as Keep-Awake#. This pin then needs to be tied to ground on the PCB. The FT_Prog utility can be used to configure the CBUS pin.

Package specific:

The effected packages are listed in Table 5.

Package	Applicable (Yes/No)
FT220XQ	Y
FT220XS	Y

Table 5

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3.2 Revision B

3.2.1 USB Data Transfer

Introduction:

An issue has been identified where the transfer of data over USB stops unexpectedly.

Problem:

The device is put into suspend mode during a transfer of certain data patterns most notable with binary zeros. This can halt the data transfer in certain circumstances and will require the device to be re-enumerated to recover.

Workaround:

This issue can be avoided by utilising the keep awake function of the chip. This will disable the USB suspend function of the chip and is therefore an intermediate workaround until revision D silicon is released with a permanent fix.

NB. With the workaround the chip will never enter lower powered suspend. However the keep awake current will be approximately 3mA.

To enable the keep awake function in the EEPROM, one of the CBUS pins needs to be configured as Keep-Awake#. This pin then needs to be tied to ground on the PCB. The FT_Prog utility can be used to configure the CBUS pin.

Package specific:

The effected packages are listed in Table 6.

Package	Applicable (Yes/No)
FT220XQ	Y
FT220XS	Y

Table 6

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3.3 Revision C

3.3.1 USB Data Transfer

Introduction:

An issue has been identified where the transfer of data over USB stops unexpectedly.

Problem:

The device is put into suspend mode during a transfer of certain data patterns most notable with binary zeros. This can halt the data transfer in certain circumstances and will require the device to be re-enumerated to recover.

Workaround:

This issue can be avoided by utilising the keep awake function of the chip. This will disable the USB suspend function of the chip and is therefore an intermediate workaround until revision D silicon is released with a permanent fix.

NB. With the workaround the chip will never enter lower powered suspend. However the keep awake current will be approximately 3mA.

To enable the keep awake function in the EEPROM, one of the CBUS pins needs to be configured as Keep-Awake#. This pin then needs to be tied to ground on the PCB. The FT_Prog utility can be used to configure the CBUS pin.

Package specific:

The effected packages are listed in Table 7.

Package	Applicable (Yes/No)
FT220XQ	Y
FT220XS	Y

Table 7

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4 Electrical and Timing specification deviations of FT220X

4.1 Revision A

No known issues at revision A

4.2 Revision B

4.2.1 Internal 3V3 Regulator

Introduction:

The FT220X uses an internal regulator to generate 3V3 from a 5V source (VCC). The source should be variable from 3V3 to 5V.

Problem:

The VCC supply to the regulator must not drop below 4.3V for the correct 3V3 regulated output to be produced.

Workaround:

VCC must not be supplied below 4.3V.

Package specific:

The effected packages are listed in Table 8.

Package	Applicable (Yes/No)
FT220XQ	Y
FT220XS	Y

Table 8

4.3 Revision C

No known issues at revision C

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5 FT220X Package Markings

FT220X is available in a RoHS Compliant RoHS Compliant package, 16 pin QFN and 16 pin SSOP. An example of the markings on the package is shown in Figure 5.1.

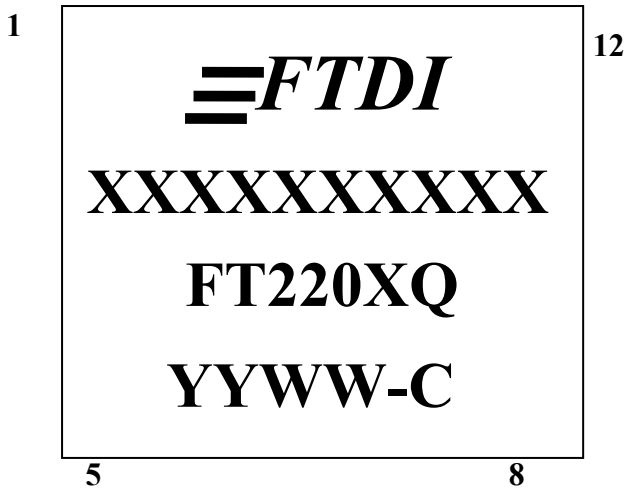

Figure 5-1 Package Markings – FT220XQ

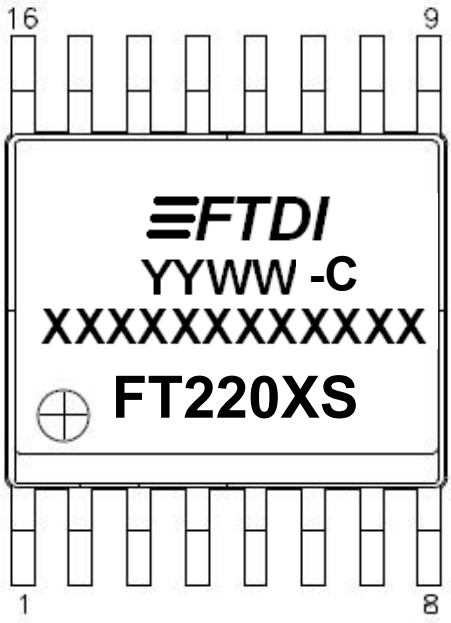

Figure 5-2 Package Markings – FT220XS

The date code format is **YYWW** where WW = 2 digit week number, YY = 2 digit year number. This is followed by the revision number.
The code **XXXXXXXXXXX** is the manufacturing LOT code

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6 Contact Information

Head Office – Glasgow, UK

Future Technology Devices International Limited
Unit 1, 2 Seaward Place, Centurion Business Park
Glasgow G41 1HH
United Kingdom
Tel: +44 (0) 141 429 2777
Fax: +44 (0) 141 429 2758

E-mail (Sales) sales1@ftdichip.com
E-mail (Support) support1@ftdichip.com
E-mail (General Enquiries) admin1@ftdichip.com

Branch Office – Hillsboro, Oregon, USA

Future Technology Devices International Limited
(USA)
7235 NW Evergreen Parkway, Suite 600
Hillsboro, OR 97123-5803
USA
Tel: +1 (503) 547 0988
Fax: +1 (503) 547 0987

E-Mail (Sales) us.sales@ftdichip.com
E-Mail (Support) us.support@ftdichip.com
E-Mail (General Enquiries) us.admin@ftdichip.com

Branch Office – Taipei, Taiwan

Future Technology Devices International Limited
(Taiwan)
2F, No. 516, Sec. 1, NeiHu Road
Taipei 114
Taiwan , R.O.C.
Tel: +886 (0) 2 8791 3570
Fax: +886 (0) 2 8791 3576

E-mail (Sales) tw.sales1@ftdichip.com
E-mail (Support) tw.support1@ftdichip.com
E-mail (General Enquiries) tw.admin1@ftdichip.com

Branch Office – Shanghai, China

Future Technology Devices International Limited
(China)
Room 1103, No. 666 West Huaihai Road,
Shanghai, 200052
China
Tel: +86 21 62351596
Fax: +86 21 62351595

E-mail (Sales) cn.sales@ftdichip.com
E-mail (Support) cn.support@ftdichip.com
E-mail (General Enquiries) cn.admin@ftdichip.com

Web Site

http://ftdichip.com

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Appendix C – Revision History

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Version 1.0	First Release	09/03/2012
Version 1.1	Added rev C and Updated China address	11/06/2012
Version 1.2	Added USB data transfer issue	12/07/2012

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