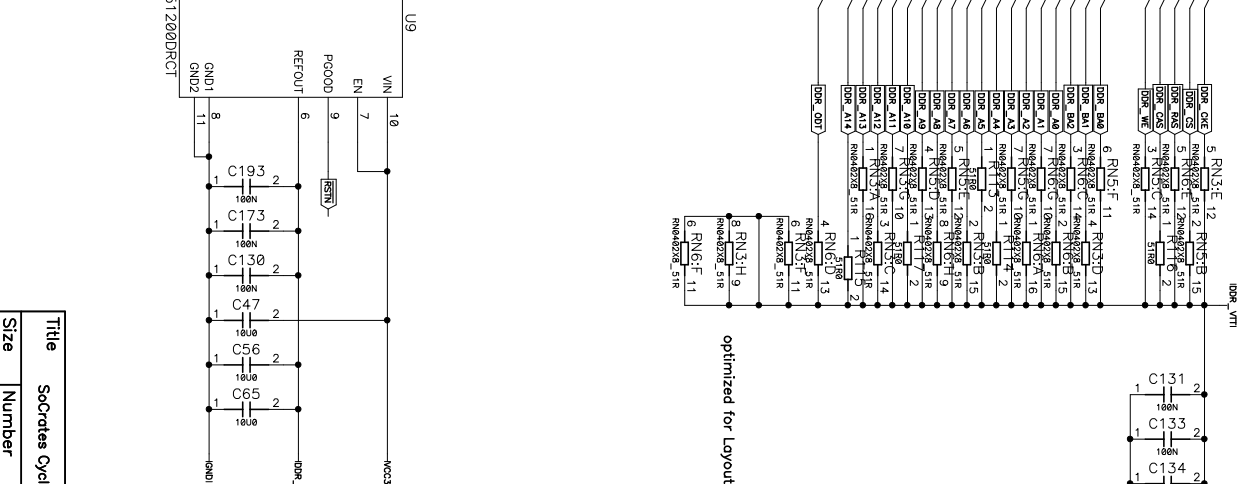
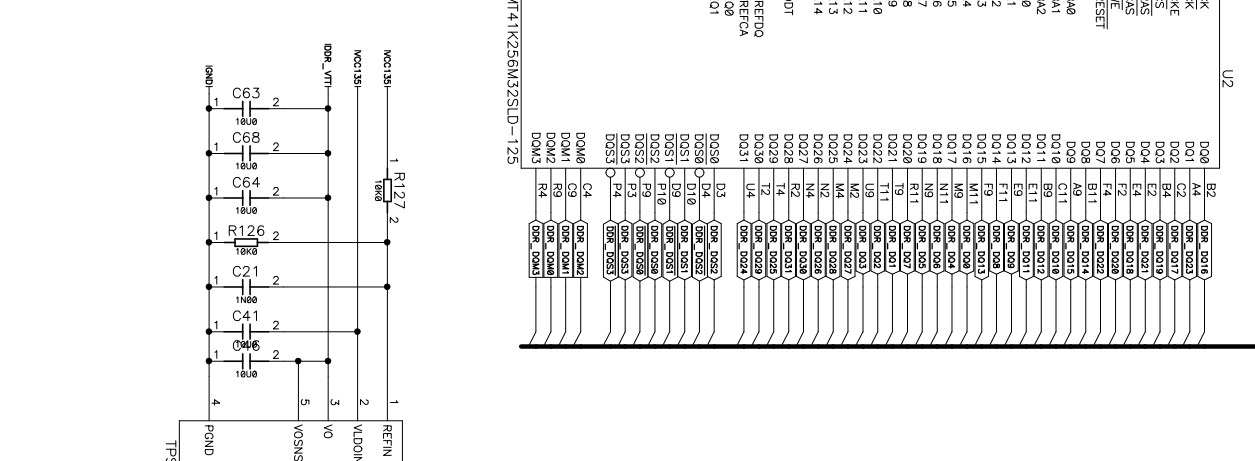
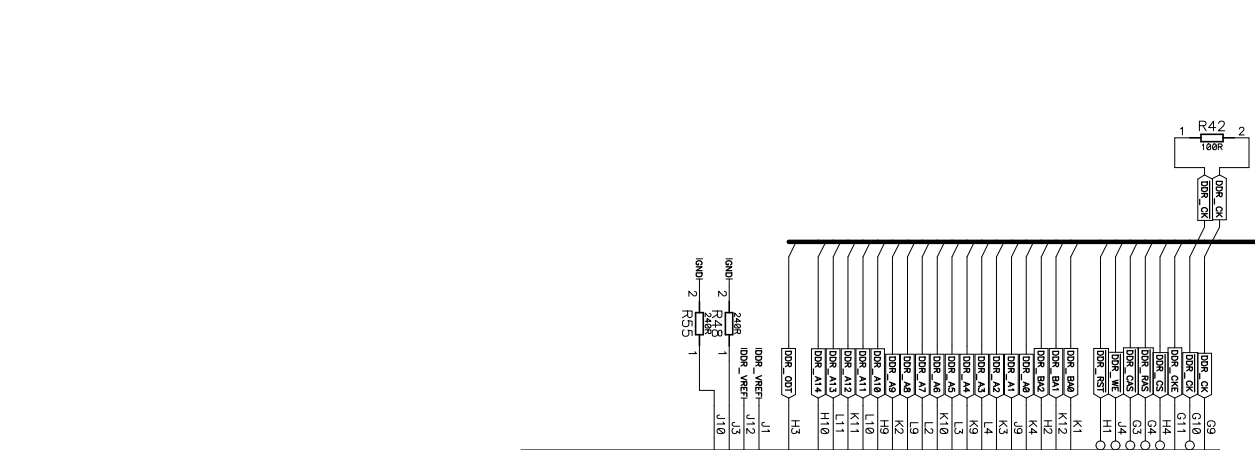
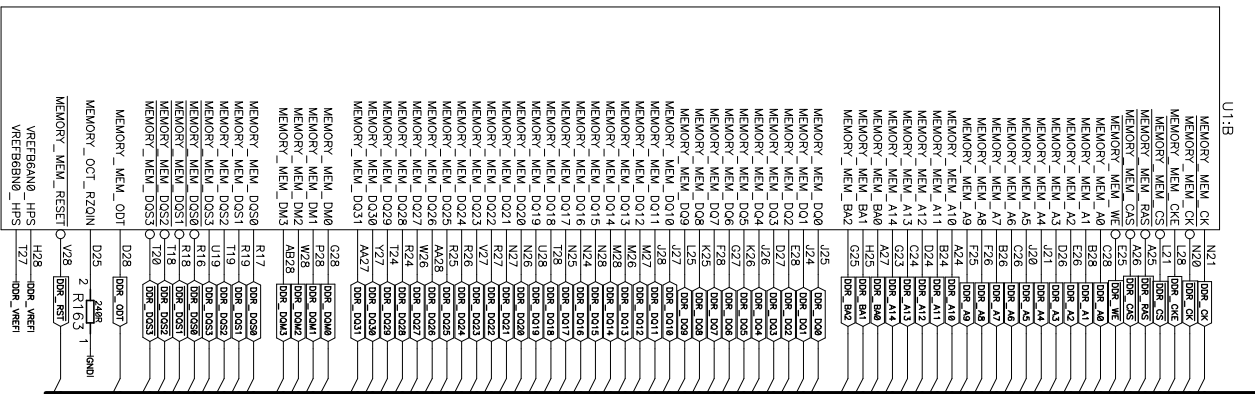


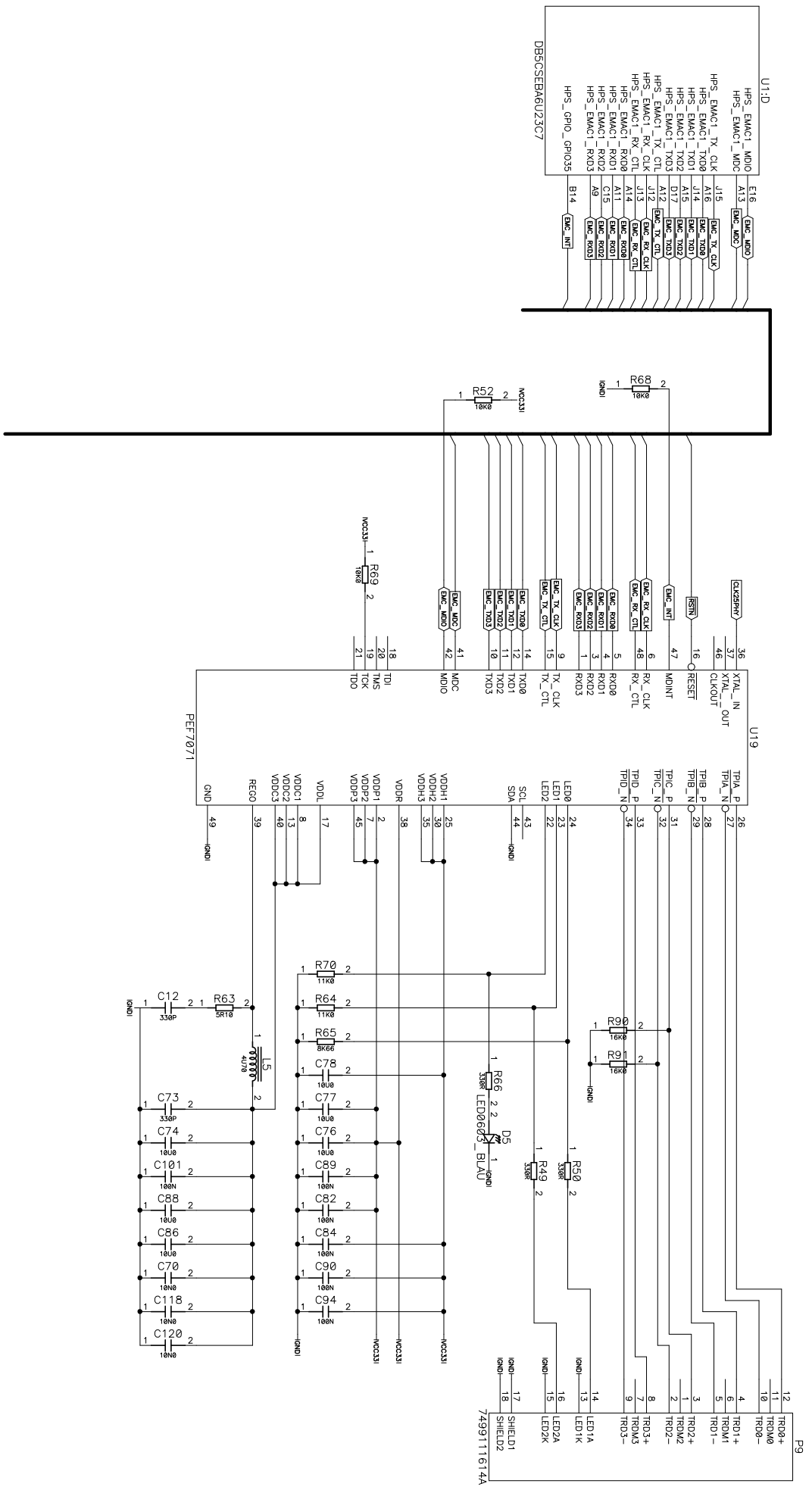
Clock Generation

Title		SoCrtas Cyclone V-Soc Evaluation Board	
Size	Number	Rev	
A3	SoCrtas	1.1	
Date	09.03.2013	Drawn by	
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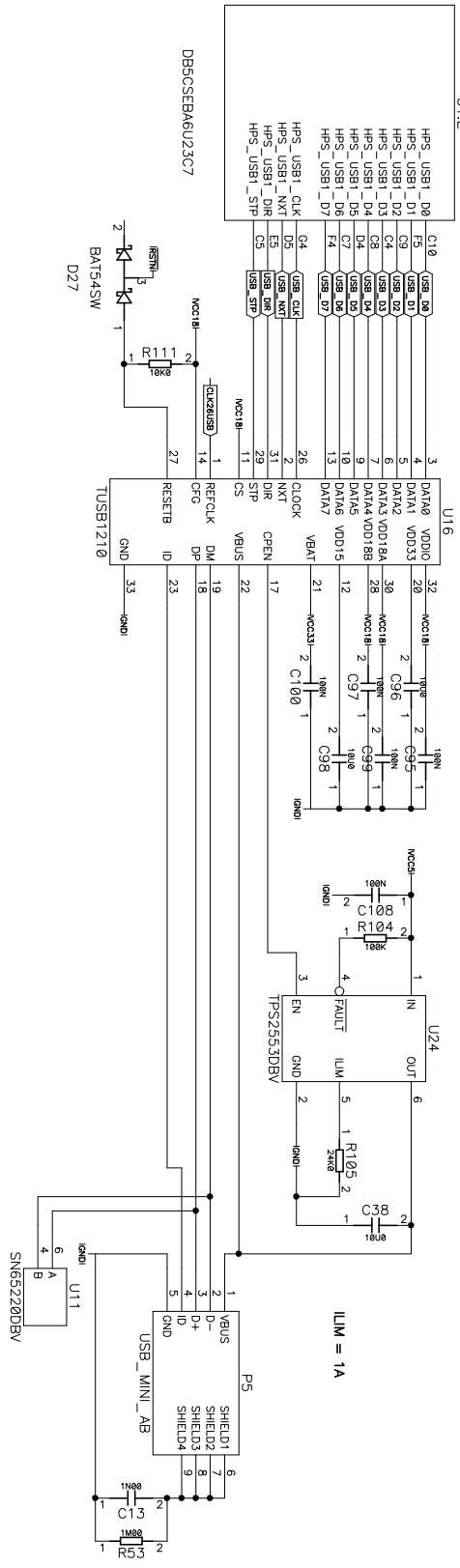
optimized for layout





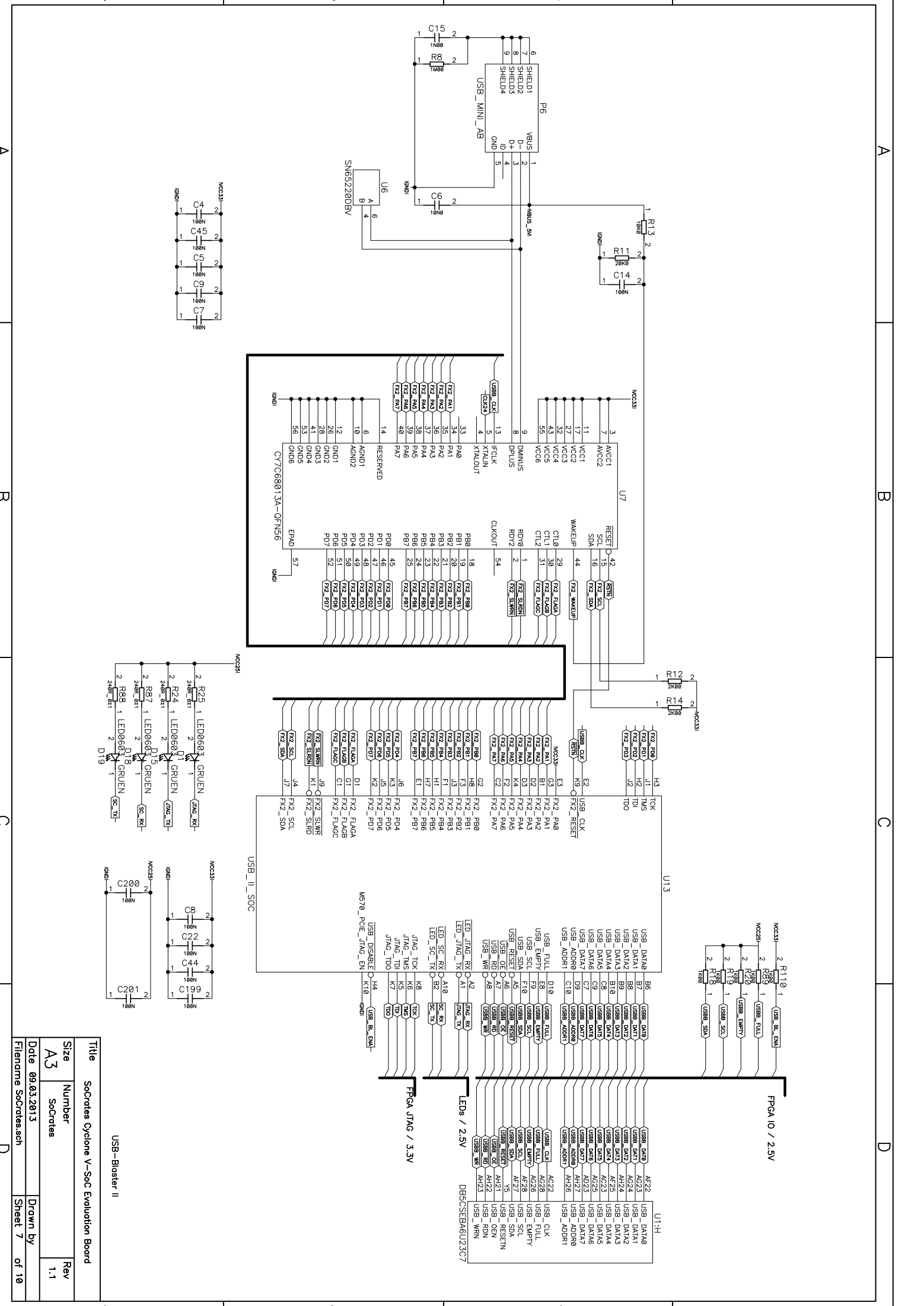
HPS Gigabit Ethernet Phy

Title		SoCrtcs Cyclone V-Soc Evaluation Board	
Size	Number	Rev	
A3	SoCrtcs	1.1	
Date	09.03.2013		Drawn by
Filename	SoCrtcs.sch		Sheet 5 of 10

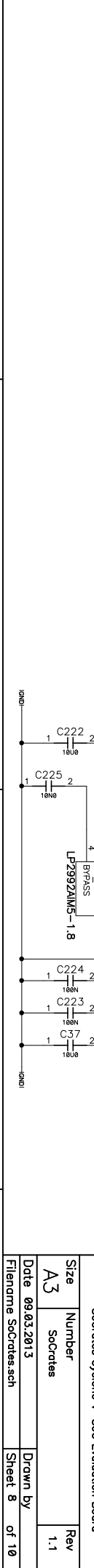
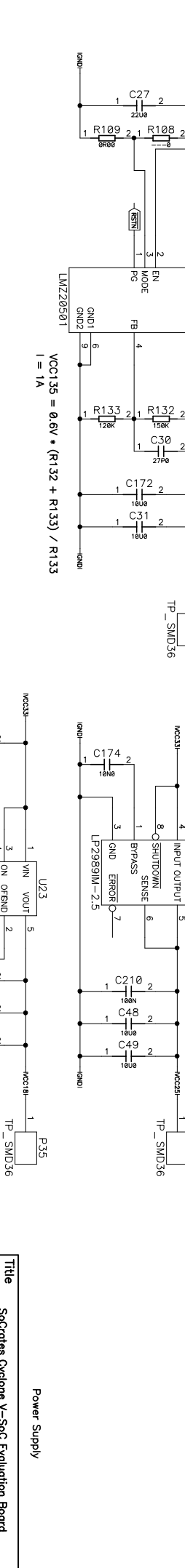
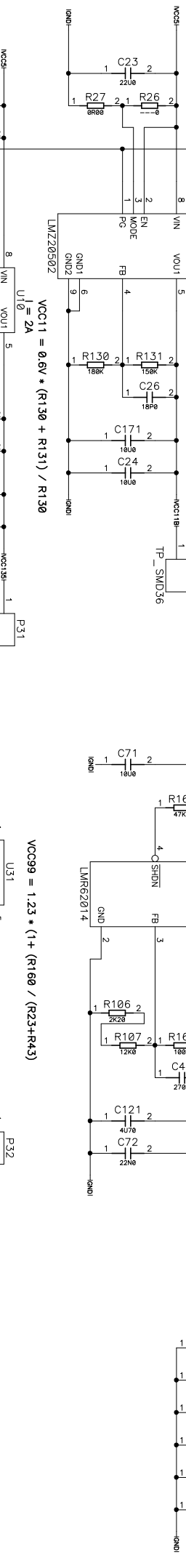
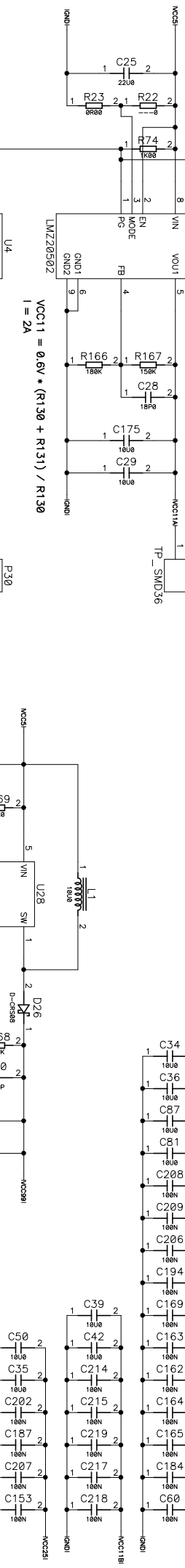
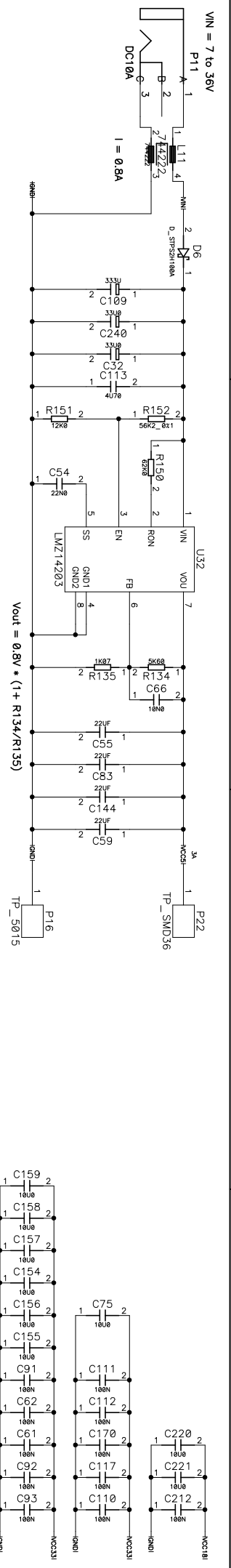


HPS - USB Interface

Title		SoCrtcs Cyclone V-Soc Evaluation Board	
Size	Number	Rev	
A3	SoCrtcs	1.1	
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09.03.2013	SoCrtcs		
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Title		SoCretes Cyclone V-Soc Evaluation Board	
Size	Number	Rev	
A3	SoCretes	1.1	
Date	09.03.2013	Drawn by	
Filename	SoCretes.sch	Sheet	7 of 10

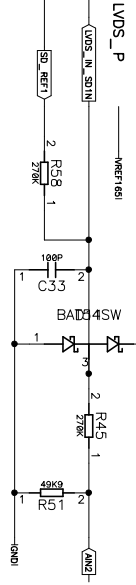
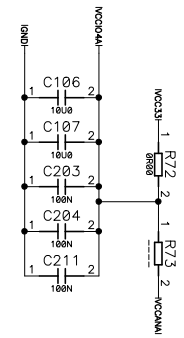
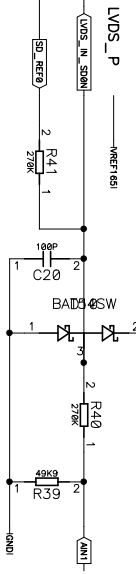
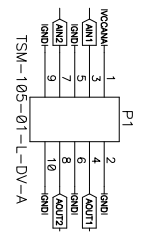
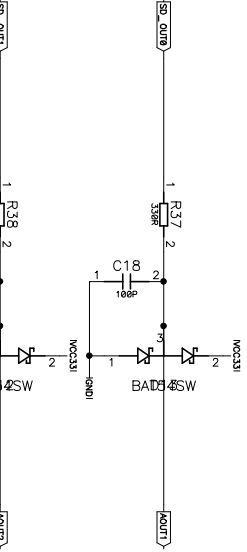
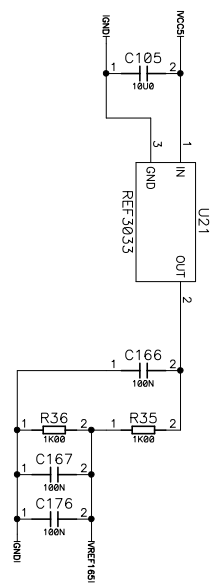


Title		SoCrtcs Cyclone V-Soc Evaluation Board	
Size	Number	Rev	
A3	SoCrtcs	1.1	
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Power Supply







SD-ADC / DAC

Title		SoCrtas Cyclone V-Soc Evaluation Board	
Size	Number	Rev	Rev
A3	SoCrtas	1.1	1.1
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