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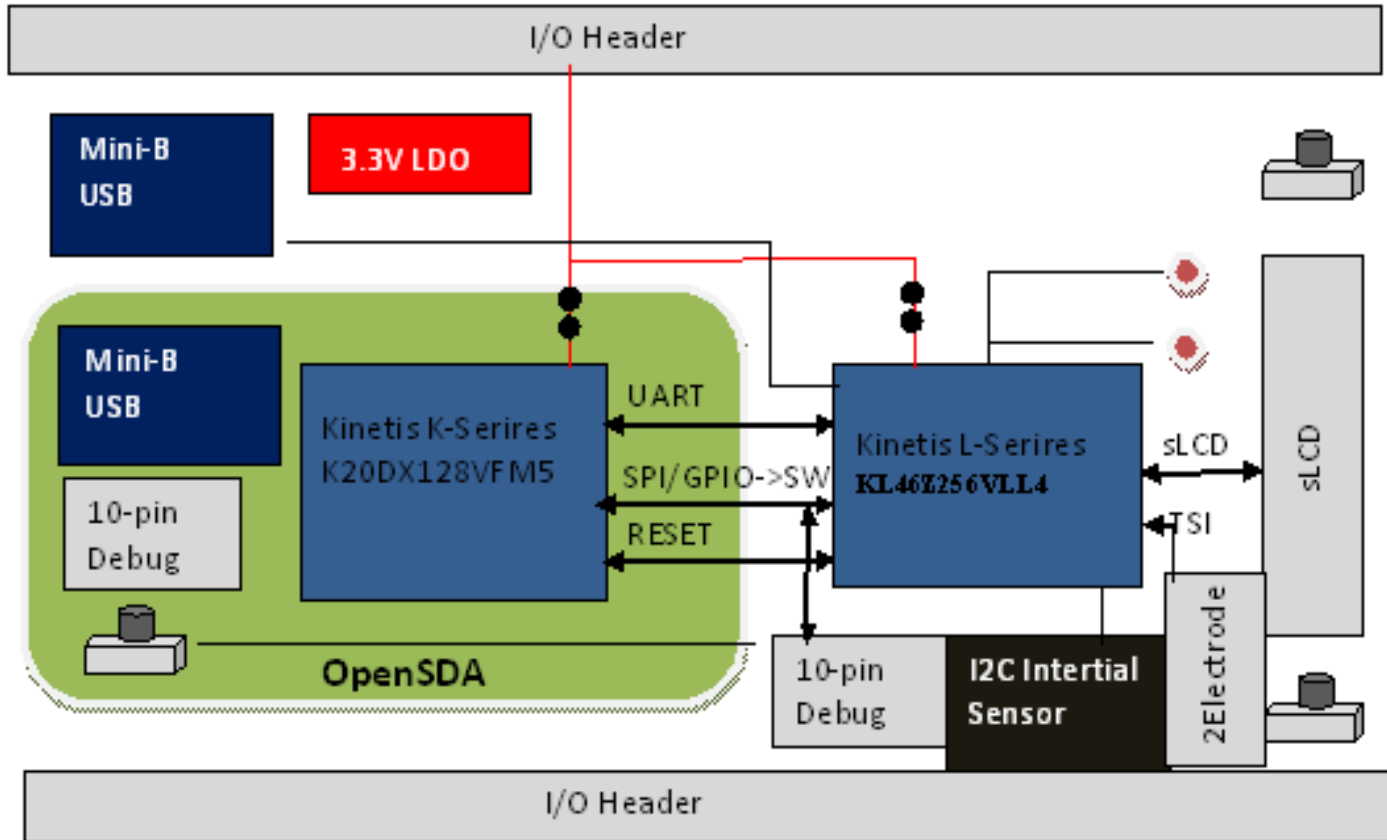
# FRDM-KL46Z

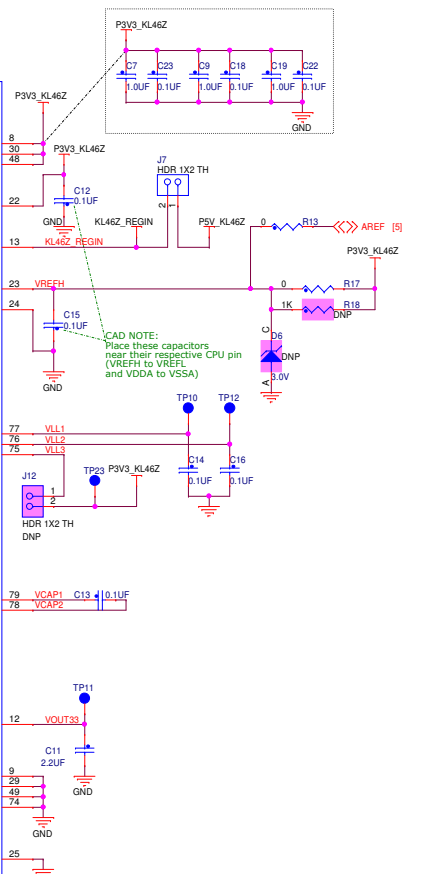
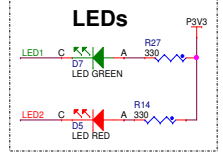
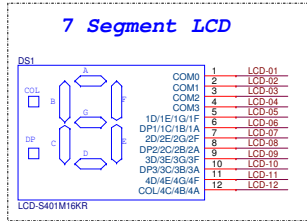
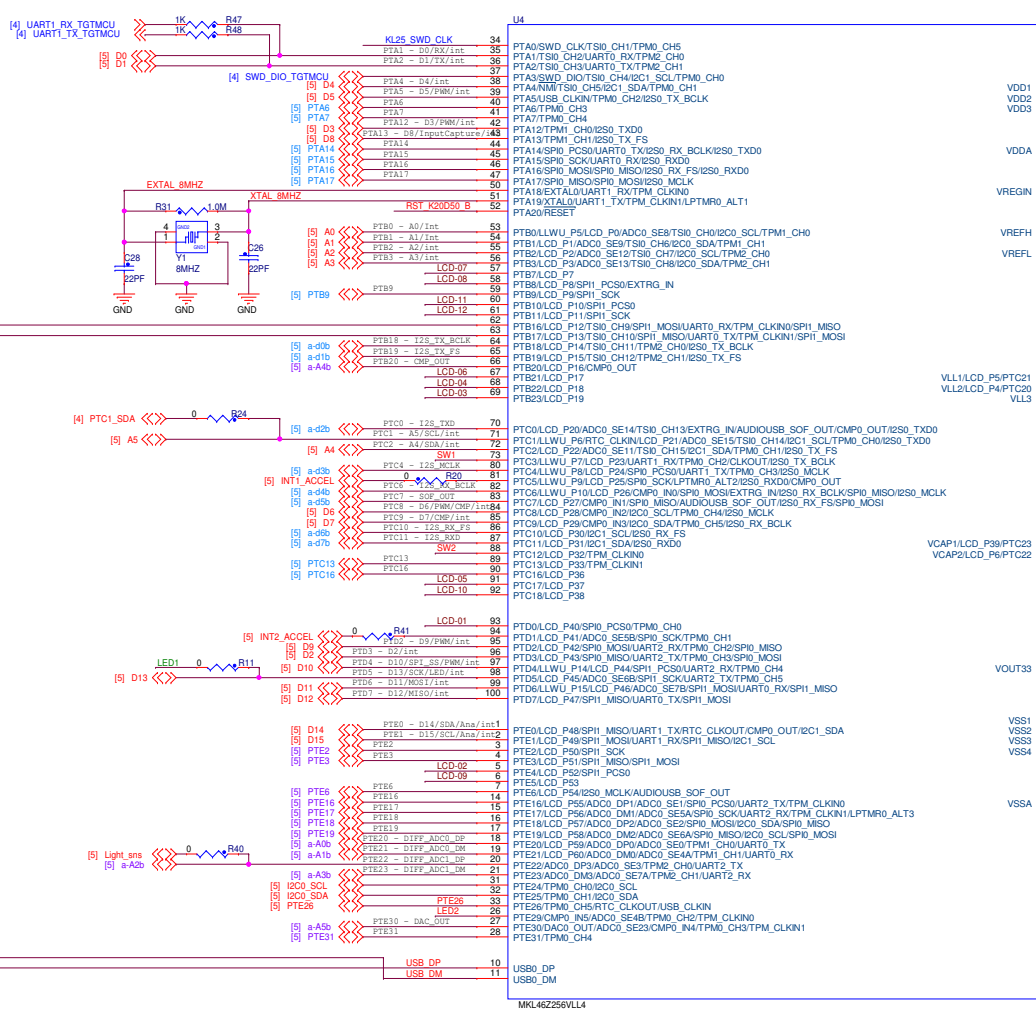
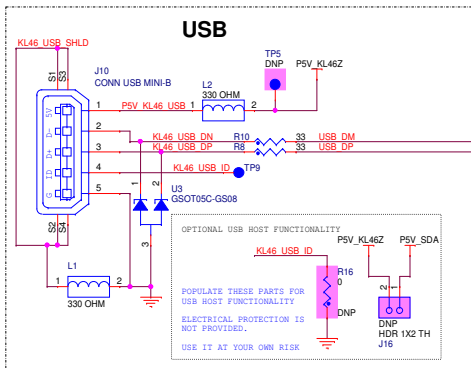
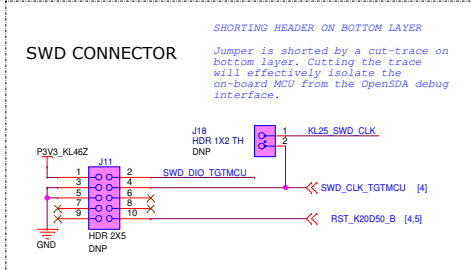
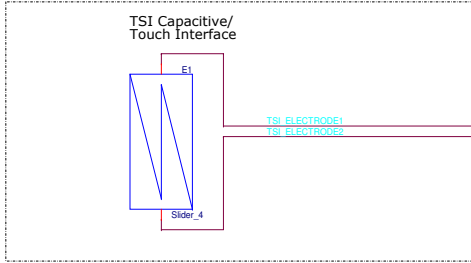
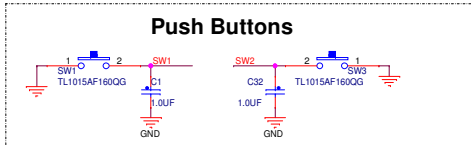
**Revisions**

Rev	Description	Date	Approved
A	Initial version		
AX1	1. As per the new FRDM-KL46 pin assignment xls connections changed. 2. Net changed from SWD_CLKto SWD_CLK_TGTMCU in connector J11.2 3. Net name TSI0_CH9/TSI0_CH10 removed from touch interface. 4. Port name and Arduino functions name Text added to the nets. 5. 5V regulator PAD added 6. separate Accelerometer and Magnetometer sensor Freplaced with FXOS8700CQ 7. PPL Released	07-MAR-13	Luis Puebla Palma
AX2	1. As per the FRDM-KL46 new pinout assignment Rev3.xls connections changed. 2. Port name and Arduino functions name Text changed to the nets as per new pin assignment. 3. Arduino connector Ref des changed as per TRD requirement 4. INT1_MAG net removed 5. Test point added in PTE26	14-MAR-13	Luis Puebla Palma
AX3	1. Magnetometer circuit added with same I2C Bus (I2C0_SCL/SDA) 2. Magnetometer interrupt is connected to Accelerometer interrupt 2 through a 0 ohms resistor. 3. Spare buffer U5B input is pulled low using a 0 ohms res.	25-MAR-13	Luis Puebla Palma
AX4	1. FXOS8700CQ replaced with MMA8451Q. NC Pins wired to make compatible with CRST, RST, RSVD1/2 in FXOS8700CQ 2. DNP 0 ohm resistor added between Inertial sensor 3rd pin and GND. 3. DNP 0 ohms resistor added b/w Inertial sensor 16th pin and PTE26	26-MAR-13	Luis Puebla Palma
B	1. DNP Updated 2. RST_K20D50 renamed to RST_K20D50_B 3. A085 Release	28-MAR-13	Luis Puebla Palma
B1	1. D6 - DNP 2. J7 - MOUNT 3. Schematic title changed to FRDM-KL46Z 4. A085 Release	03-MAY-13	Luis Puebla Palma
C	Changing C17 from 1uF to 0.1uF to fix yield issue	19-JUN-13	Luis Puebla Palma

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Designer: JOHN PAUL FRANKLIN		ICAP Classification: FCP: FUC: PUB: X	
Drawn by: JOHN PAUL FRANKLIN		Drawing Title: <b>FRDM-KL46Z</b>	
<b>TITLE PAGE</b>			
Approved: Luis Puebla Palma	Size C	Document Number SCH-27822   PDF: SPF-27822	Rev C
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- Unless Otherwise Specified:  
 All resistors are in ohms, 5%, 1/8 Watt  
 All capacitors are in uF, 20%, 50V  
 All voltages are DC  
 All polarized capacitors are aluminum electrolytic
- Interrupted lines coded with the same letter or letter combinations are electrically connected.
- Device type number is for reference only. The number varies with the manufacturer.
- Special signal usage:  
 \_B Denotes - Active-Low Signal  
 <> or [] Denotes - Vectored Signals
- Interpret diagram in accordance with American National Standards Institute specifications, current revision, with the exception of logic block symbology.





ICAP Classification: FCP, FIUC, PUB: X  
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# ARDUINO HEADERS

- [3] D0 PTA1 - D0/RX/int
- [3] D1 PTA2 - D1/TX/int
- [3] D2 PTA3 - D2/int
- [3] D3 PTA12 - D3/PWM/int
- [3] D4 PTA4 - D4/int
- [3] D5 PTA5 - D5/PWM/int
- [3] D6 PTC8 - D6/PWM/CHP/int
- [3] D7 PTC9 - D7/CHP/int
- [3] D8 PTA13 - D8/InputCapture/int
- [3] D9 PTD4 - D9/PWM/int
- [3] D10 PTD4 - D10/SPI-SS/PWM/int
- [3] D11 PTD6 - D11/MISO/int
- [3] D12 PTD5 - D12/SSK/LED/int
- [3] D15 PTE0 - D14/SDA/Ana/int
- [3] D15 PTE1 - D15/SCL/Ana/int

